

<p style="text-align: right;">O I P E FEB 20 2001 U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</p> <p>SEARCHED SERIALIZED INDEXED FILED RECEIVED U. S. PATENT AND TRADEMARK OFFICE BE SEVERAL SHEETS IF NECESSARY)</p>	<p>ATTY. DOCKET NO. IMEC193.001AUS</p>	<p>APPLICATION NO. 09/760129</p>
	<p>APPLICANT Miranda, et al.</p>	
	<p>FILING DATE January 12, 2001</p>	<p>GROUP 2122</p>

## U.S. PATENT DOCUMENTS

#### FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
•							
•							

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	Gupta, et al., Analysis of High-Level Address Code Transformations for Programmable Processors, Department of Information and Computer Science, University of California at Irvine, IMEC Lab., Kapeldreef 75, 3001 Leuven, Belgium
ARG	Liem, et al., Address Calculation for Retargetable Compilation and Exploration of Instruction-Set Architecture, Proceedings 33rd Design Automation Conference, 1996. <u>pp. 597-600</u>
ARG	Miranda, et al., High-level Address Optimisation and Synthesis Techniques for Data-Transfer Intensive Applications, IEEE Transactions on VLSI Systems, Vol. 6, No. 4, pp. 677-686, December 1998.

S:\DOCS\EMN\EMN-6777.DOC  
021401

EXAMINER Andre R. Fowles DATE CONSIDERED 12/24/03

\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.